

[IEEE HOME](#) | [SEARCH IEEE](#) | [SHOP](#) | [WEB ACCOUNT](#) | [CONTACT IEEE](#)[Membership](#) [Publications/Services](#) [Standards](#) [Conferences](#) [Careers/Jobs](#)**IEEE Xplore™**
RELEASE 1.3[Help](#) [FAQ](#) [Terms](#) [IEEE Peer](#) [Quick Links](#)[Review](#)

Welcome to IEEE Xplore™

- ☐ Home
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account

 Print FormatYour search matched **15** of **753342** documents.Results are shown **25** to a page, sorted by **publication year** in **descending** order.

You may refine your search by editing the current search expression or entering a new one the te

Then click **Search Again**.**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****1 Fast computations on a low-cost DSP-based shared-memory multiprocessor system***Christou, C.S.*

Electronics, Circuits and Systems, 2000. ICECS 2000. The 7th IEEE International Conference on , Volume: 1 , 2000

Page(s): 189 -192 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(352 KB\)\]](#) **CNF****2 A single-chip, 1.6-billion, 16-b MAC/s multiprocessor DSP***Ackland, B.; Anesko, A.; Brinthaup, D.; Daubert, S.J.; Kalavade, A.; Knobloc Micca, E.; Moturi, M.; Nicol, C.J.; O'Neill, J.H.; Othmer, J.; Sackinger, E.; Sing Sweet, J.; Terman, C.J.; Williams, J.*

Solid-State Circuits, IEEE Journal of , Volume: 35 Issue: 3 , March 2000

Page(s): 412 -424

[\[Abstract\]](#) [\[PDF Full-Text \(828 KB\)\]](#) **JNL****3 A single-chip 1.6 billion 16-b MAC/s multiprocessor DSP***Ackland, B.; Anesko, A.; Brinthaup, D.; Daubert, S.J.; Kalavade, A.; Knobloc Micca, E.; Moturi, M.; Nicol, C.J.; O'Neill, J.H.; Othmer, J.; Sackinger, E.; Sing Sweet, J.; Terman, C.J.; Williams, J.*

Custom Integrated Circuits, 1999. Proceedings of the IEEE 1999 , 1999

Page(s): 537 -540

[\[Abstract\]](#) [\[PDF Full-Text \(420 KB\)\]](#) **CNF****4 Software environment for a multiprocessor DSP**

Kalavade, A.; Othmer, J.; Ackland, B.; Singh, K.J.

Design Automation Conference, 1999. Proceedings. 36th , 1999

Page(s): 827 -830

[\[Abstract\]](#) [\[PDF Full-Text \(428 KB\)\]](#) **CNF**

5 An implementation of a parallel ray tracing algorithm on hybrid para architecture

Chang-Geun Kwon; Hyo-Kyung Sung; Heung-Moon Choi

Acoustics, Speech and Signal Processing, 1998. Proceedings of the 1998 IEEE International Conference on , Volume: 3 , 1998

Page(s): 1745 -1748 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(316 KB\)\]](#) **CNF**

6 High-speed image reconstruction based on CBP and Fourier inversio methods

Rajan, K.; Patnaik, L.M.; Ramakrishna, J.

High Performance Computing on the Information Superhighway, 1997. HPC A 1997

Page(s): 401 -406

[\[Abstract\]](#) [\[PDF Full-Text \(476 KB\)\]](#) **CNF**

7 Parallelization of 3-D PET BpJF reconstruction on a DSP cluster

Di Lecce, V.; Di Sciascio, E.; Manni, A.R.

Nuclear Science Symposium and Medical Imaging Conference Record, 1995., , Volume: 2 , 1995

Page(s): 1222 -1226 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(480 KB\)\]](#) **CNF**

8 Design and implementation of an ordered memory access architectu

Sriram, S.; Lee, E.A.

Acoustics, Speech, and Signal Processing, 1993. ICASSP-93., 1993 IEEE Inter Conference on , Volume: 1 , 1993

Page(s): 345 -348 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(388 KB\)\]](#) **CNF**

9 Extensible digital-signal-processing modules for real-time control an simulation

Yan Guo; Lee, H.C.; Boon-Teck Ooi

Industrial Electronics, Control, and Instrumentation, 1993. Proceedings of the

'93., International Conference on , 1993

Page(s): 2229 -2234 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(484 KB\)\]](#) **CNF**

10 HARP: an open architecture for parallel matrix and signal processin

Dowling, E.M.; Fu, Z.; Drafz, R.S.

Parallel and Distributed Systems, IEEE Transactions on , Volume: 4 Issue: 10
1993

Page(s): 1081 -1091

[\[Abstract\]](#) [\[PDF Full-Text \(1004 KB\)\]](#) **JNL**

11 A universal DSP system based on TMS320C25

Yin Jian-Qi; Yang Hong-Kui

Circuits and Systems, 1991. Conference Proceedings, China., 1991 Internatio
Conference on , 1991

Page(s): 774 -777 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(312 KB\)\]](#) **CNF**

12 Image compression using coarse grain parallel processing

Normile, J.; Wright, D.

Acoustics, Speech, and Signal Processing, 1991. ICASSP-91., 1991 Internatio
Conference on , 1991

Page(s): 1121 -1124 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(428 KB\)\]](#) **CNF**

13 ABACO architecture: implementation details

Beltran, F.A.; Navarro, J.

Electrotechnical Conference, 1991. Proceedings., 6th Mediterranean , 1991

Page(s): 1272 -1275 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(220 KB\)\]](#) **CNF**

**14 A multi-DSP board for a parallel computer using a packet switched,
pipelined bus**

Distefano, E.M.; Snelgrove, W.M.

Communications, Computers and Signal Processing, 1989. Conference Procee
IEEE Pacific Rim Conference on , 1989

Page(s): 156 -160

[\[Abstract\]](#) [\[PDF Full-Text \(488 KB\)\]](#) **CNF**

15 A configurable multiprocessor system for DSP behavioral simulatio

Koh, W.; Yeung, A.; Hoang, P.; Rabaey, J.

Circuits and Systems, 1989., IEEE International Symposium on , 1989

Page(s): 1403 -1406 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(344 KB\)\]](#) **CNF**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2002 IEEE — All rights reserved